

IN THE SPECIFICATION:

Please amend the specification as follows:

Page 10, beginning on line 10:

Referring to FIG. 6, the ~~peak-bottom~~ detector 300 in the burst mode optical receiver includes an amplifying terminal 310 for reducing an offset of a bottom value, a diode 320, a peak hold capacitor (hereinafter, referred to as Cp) 330 for charging a bottom value, a signal amplitude detector 340 for monitoring a signal amplitude and generating a control voltage corresponding to the signal amplitude and a current source 350 for driving an actual current according to an output of the signal amplitude detector 340.

Page 11, beginning on line 5:

In comparison with the ~~bottom-peak~~ detector 300, the bottom detector 300 has the polarity of the diode 320 is in a reverse state and the Cp 330 is connected to Vcc and not ground. In the ~~peak-bottom~~ detector 300, when a signal decreases in a negative direction, the diode 320 becomes forward biased. Then a charge occurs through the Cp 330. When the signal increases in a positively, the diode 320 becomes reverse biased. Then the Cp 330 discharges the current that has been charged. When the size of the signal detected by the signal amplitude detector 340 is small, the discharge path becomes open. In this manner, overcharge of the Cp 330 is prevented. When the size of the signal detected by the signal amplitude detector 340 is large, the discharge path becomes closed. Thus, the charge time is faster.